



DOCKET NO. WIELSMA 3

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of:

Jan Wielsma

Serial No.: 10/675,195

Filed: September 30, 2003

For: LOW-LOSS PRINTED CIRCUIT BOARD ANTENNA
STRUCTURE AND METHOD OF MANUFACTURE THEREOF

Group No.: 2828

Examiner: James Vannucci

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on
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In accordance with 37 C.F.R. §1.56, and in accordance with the provisions of 37 C.F.R. §§1.97 and 1.98 and §609 of the Manual of Patent Examining Procedure, Applicant hereby makes an additional disclosure of the patents, publications and other information listed below and on the accompanying form PTO-1449, which references are considered to be potentially material to the patentability of the invention disclosed in the above-referenced application. Copies of the listed references are submitted herewith. Disclosure has been filed previously on September 30, 2003 and June 15, 2004.

U.S. Patent No.

Inventor

Date

6,204,814 B1

Rothe

March 20, 2001

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above patents and to question the relevance and materiality of the patents listed herein, in whole, in part or in combination, subsequent to filing this Information Disclosure Statement.

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Respectfully submitted,

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First Named Inventor	Jan Wielsma
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Examiner Name	James Vannucci
Attorney Docket Number	WIELSMA 3

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Examiner Signature		Date Considered	
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